IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re l	U.S. Patent Application of)
NAKA	AKURA et al.)
Appli	cation Number: To be assigned)
Filed:	Concurrently Herewith)
For:	METHOD OF FABRICATING PRINTED CIRCUIT BOARD))
ATTO	RNEY DOCKET NO. TESD.0027	<i>)</i>

Honorable Assistant Commissioner for Patents Washington, D.C. 20231

REQUEST FOR PRIORITY UNDER 35 U.S.C. § 119 AND THE INTERNATIONAL CONVENTION

Sir:

In the matter of the above-captioned application for a United States patent, notice is hereby given that the Applicant claims the priority date of December 17, 2002, the filing date of the corresponding Japanese patent application 2002-365651.

A certified copy of Japanese patent application 2002-365651 will be submitted upon receipt from Applicant. Acknowledgment of receipt of the certified copy is respectfully requested in due course.

Respectfully submitted,

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REED SMITH LLP

3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200 December 16, 2003